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JAN 13 2005  
**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCEDURE REQUESTED  
EXAMINING GROUP 1774**

PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.1952-01

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
Yoshitaka TSUNASHIMA et al. ) Group Art Unit: 2814  
Application No.: 09/688,989 ) Examiner: Shrinivas H. Rao  
Filed: October 17, 2000 ) Confirmation No.: 2408  
For: SEMICONDUCTOR DEVICE AND )  
METHOD OF MANUFACTURING )  
THE SAME )

**Mail Stop AF**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**AMENDMENT AFTER FINAL**

In reply to the Final Office Action mailed August 13, 2004, the period for response having been extended to January 13, 2005, by a two-month request for extension of time and fee payment filed concurrently herewith, and pursuant to 37 C.F.R. § 1.116, Applicants propose to amend the above captioned patent application as follows:

**Amendments** are included in the attached replacement drawing sheets. The amendments are discussed at page 3 of this paper.

**Remarks** begin at page 3 of this paper.

**Attachments** to this paper include drawing replacement sheets pages 1-36.

*Fee only*